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04743 7590 05/21/2004

MARSHALL, GERSTEIN & BORUN LLP
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Michael R. Hull

(Depositor's name)

(Signature)

08 July 04

(Date)

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/630,280 | 07/30/2003 | Jae-Han Cha | 29925/39488 | 9703 |

TITLE OF INVENTION: METHOD FOR FABRICATING BICMOS TRANSISTOR

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|-----------|-----------------|------------------|------------|
| nonprovisional | NO | \$1330 | \$300 | \$1630 | 08/23/2004 |

| EXAMINER | ART UNIT | CLASS-SUBCLASS |
|-------------|----------|----------------|
| LEE, CALVIN | 2825 | 438-314000 |

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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

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1 Marshall,

2 Gerstein &

3 Borun LLP

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Hynix Semiconductor Inc.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Kyungki-do, Republic of Korea

Please check the appropriate assignee category or categories (will not be printed on the patent); individual corporation or other private group entity government

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